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<b>FORM PTO-1449 U.S. Department of Commerce (Rev. 4/92) Patent and Trademark Office</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <small>(Use several sheets if necessary)</small>								ATTY. DOCKET NO.	SERIAL NO.
								520.33330CC3	Not yet assigned
								APPLICANT <b>M. TSUCHIDA, et al</b>	
FILING DATE <b>September 19, 2000</b>				GROUP <b>2777</b>					

**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NUMBER							DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		5	5	0	4	8	9	4					
		5	5	4	4	3	1	3	8/96	Shachnai et al	395	200.01	
		5	5	4	8	7	6	1	8/96	Balasundaram et al	395	700	
		5	5	5	1	0	2	7	8/96	Choy et al	395	600	
		5	5	2	4	2	5	7	6/96	Koike et al	395	800	
		5	5	1	5	5	3	1	5/96	Fujiwara et al	395	600	
		4	3	4	4	1	3	4	8/82	Barnes	364	200	
		5	2	8	3	7	3	1	2/94	Lalonde et al	364	401	
		5	2	8	3	8	9	7	2/97 <sup>VX</sup>	Georgiadis et al	395	650	

**FOREIGN PATENT DOCUMENTS**

		DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUBCLASS	ABSTRACT		
													YES	NO	
		0	5	2	6	1	0	8	5	10/93	Japan	H04 M3	42		
		0	5	0	3	6	3	4	3	2/93	Japan	G06 F15	40		
		6	3	3	1	8	6	2	8	12/88	Japan			xx	
		6	3	1	4	2	9	9		4/93	Japan	G06 F	15/40	xx	

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

	Hachem et al "Back End Architecture based on Transformed Inverted Lists. A Surrogate File Structure for a Very Large Data/Knowledge Base" IEEE, pp. 10-19.
	Parashar et al, "On Partitioning Dynamic Adaptive Grip Hierarchies" IEEE, pp. 604-613.
	Pande et al "Compiling Functional Parallelism on Distributed-Memory Systems", IEEE, pp. 64-76.
	Hua et al, "Dynamic Load Balancing in Multicomputer Database Systems using Partition Tuning" IEEE, pp. 968-983.

EXAMINER	/Jean Corrielus/	DATE CONSIDERED	06/18/2009

**EXAMINER:** Initial if citation is considered, draw line through citation if not in conformance and not considered.  
Include copy of this form with next communication to applicant.

(Form PTO-1449 (6-4))

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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /J.C./